

El Paso Community College

Syllabus

Part II

Official Course Description

SUBJECT AREA	<u>Engineering</u>
COURSE RUBRIC AND NUMBER	<u>ENGR 2406</u>
COURSE TITLE	<u>Introduction to Digital Systems</u>
COURSE CREDIT HOURS	<u>4 3 :<!-- 3</u--> Credits Lec Lab</u>

I. Catalog Description

Provides an introduction to theory and design of digital logic, circuits and systems including number systems, operations and codes; logic gates; Boolean Algebra and logic simplification; Karnaugh maps; combinational logic; functions of combinational logic; flip-flops and related devices; counters; shift registers; sequential logic; and memory and storage. **(3:3)**.

II. Course Objectives

Upon satisfactory completion of this course, the student will be able to

- A. Apply the number representations used in today's digital systems and their arithmetic properties and conversion techniques.
- B. Apply the fundamental concepts of the theorems of Boolean Algebra to minimize AND-OR combinational circuit realizations.
- C. Design combinational circuits (adders, code converters, etc.) using basic logic gates.
- D. Analyze the design of standard arithmetic circuits that involve signed addition, subtraction, and multiplication using combinational circuits.
- E. Analyze and design combinational digital circuits using building blocks such as multiplexers, ROMs, PLAs, PALs, and decoders.
- F. Analyze and design synchronous sequential circuits (counters, registers, etc.) by using basic flip-flops and logic gates.
- G. Explain the fundamentals of clocking logic and how to avoid the adverse affects of logic hazards.
- H. Design and test digital circuits using MSIs, EPROMs, and simple CAD tools.

III. THECB Learning Outcomes (ACGM)

Upon successful completion of this course, students will:

1. Utilize binary and hexadecimal numbers.

2. Solve problems involving digital codes, operations, and number systems.
3. Define, describe, and analyze fundamentals of Boolean algebra and digital logic gates.
4. Describe, analyze, design, and fabricate combinational logic circuits.
5. Describe, analyze, design, and fabricate sequential logic circuits.
6. Describe and explain the fundamentals of memory operations.
7. Apply computer mathematical and/or simulation tools to solve digital systems problems.

Lab Outcomes:

Upon successful completion of this course, students will:

1. Prepare laboratory reports that clearly communicate experimental information in a logical and scientific manner.
2. Conduct basic laboratory experiments involving design and construction of digital circuits and systems.
3. Relate physical observations and measurements involving digital circuits and systems to theoretical principles.
4. Evaluate the accuracy of physical measurements and the potential sources of error in the measurements.
5. Design fundamental experiments involving principles of digital circuits and systems.
6. Identify and apply appropriate sources of information for conducting laboratory experiments involving digital circuits and systems.
7. Apply computer mathematical and/or simulation tools to solve digital systems problems.

IV. Evaluation

- A. The grade for this course will be based on performance on quizzes, regular exams, labs, homework, and a comprehensive final exam. Every student is required to take the final exam at the end of the semester.
- B. The grade will be assigned based on the scale below:

<u>Score</u>	<u>Grade</u>
90-100	A
80-89	B
70-79	C
60-69	B
Below 60	F

V. Disability Statement (Americans with Disabilities Act [ADA])

EPCC offers a variety of services to persons with documented sensory, mental, physical, or temporary disabling conditions to promote success in classes. If you have a disability and believe you may need services, you are encouraged to contact the Center for Students with Disabilities to discuss your needs with a counselor. All discussions and documentation are kept confidential. Offices located: VV Rm C-112 (831-2426); TM Rm 1400 (831-5808); RG Rm B-201 (831-4198); NWC Rm M-54 (831-8815); and MDP Rm A-125 (831-7024)

VI. 6 Drop Rule

Students who began attending Texas public institutions of higher education for the first time during the Fall 2007 semester or later are subject to a 6-Drop limit for all undergraduate classes. Developmental, ESL, Dual Credit and Early College High School classes are exempt from this rule. All students should consult with their instructor before dropping a class. Academic assistance is available. Students are encouraged to

see Counseling Services if dropping because exemptions may apply. Refer to the EPCC catalog and website for additional information.

VII. Title IX and Sex Discrimination

Title 9 (20 U.S.C. 1681 & 34 C.F.R. Part 106) states the following "No person in the United States shall, on the basis of sex, be excluded from participation in, be denied the benefits of, or be subjected to discrimination under any educational program or activity receiving Federal financial assistance." The Violence Against Women Act (VAWA) prohibits stalking, date violence, sexual violence, and domestic violence for all students, employees and visitors (male and female). If you have any concerns related to discrimination, harassment, or assault (of any type) you can contact the Assistant to the Vice President for Student and Enrollment Services at 915-831-2655. Employees can call the Manager of Employee Relations at 915-831-6458. Reports of sexual assault/violence may also be reported to EPCC Police at 915-831-2200.